Fabrication of p-type transparent conducting oxide with high hardness for application as protective anode in flat panel displays

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Abstract- In this research, a p-type transparent conducting oxide with high hardness is prepared. Samples have been analysed to investigate their optical and electronic properties, crystal structure and the hardness of the layer. The formed layer was revealed to have potential applications as protective conducting glass in electronic displays. For this purpose, 2.5% Mg-doped CuCrO$_2$ delafossite target was prepared using solid-state reaction method. The target was then used to deposit a CuCrO$_2$: Mg thin layer on quartz substrates using RF sputtering. The prepared thin film that can play the role of a positive electrode in displays has a transparency in the visible region of about 67%, an electrical conductivity of 10.5 Scm$^{-1}$ and a mechanical hardness of about 950 Hv.

Keywords: Hard transparent ceramic, Transparent conducting oxide, Delafossite, Display.
1. Introduction

Transparent conducting oxides (TCOs) are an important component of many technologies such as photovoltaics, low-emission windows, electromagnetic shields and flat panel displays [1]. The above mentioned applications are made possible due to the simultaneous conductivity and optical transparency of these materials [2]. Generally, TCOs are grown as thin films that require certain substrate properties. By forming TCO with layers that have a high hardness, the need for protective thick glasses and encapsulation of the inner circuits is effaced [3].

In previous years many methods have been utilized to protect devices from oxidation, wear, and other damages made via atmospheric gas interactions. For this purpose transparent polycrystalline SiO$_2$, silicon, and glasses with high thickness up to 20 μm have been grown on top of devices [4]. Among transparent conducting oxides, n-type TCOs are more developed and the need for their p-type counterparts is sensed. Delafossite materials have shown the best simultaneous transparency and p-type conductivity [5].

CuCrO$_2$ is a delafossite material containing Cr atoms that is a refractory metal with high hardness even in the form of an oxide. Comprising Cu atoms, which lead to high electric conductivity, CuCrO$_2$ becomes a good candidate as hard TCO layer with the energy band gap of 3.25 eV [6]. In this paper, we are aiming to deposit Mg-doped CuCrO$_2$ to obtain a hard TCO. By doing so, this material will have potential features to take the place of nowadays protective glass and non-conductive thin layers used for encapsulation of some electronic devices.

2. Experiment

In order to prepare CuCr$_{0.975}$Mg$_{0.025}$O$_2$ target we employed solid-state reaction method. High-purity oxide powders of copper, chromium and magnesium were mixed in stoichiometric ratio and milled for 10 hours using a ball milling system. The resulting powder was pressed into a 2-inch pellet by a hydraulic press and sintered in an air furnace at 1250 °C for 12 hours. The target was placed in a DST3 RF sputtering system and Mg-doped CuCrO$_2$ thin film was prepared on quartz substrate. The sputtering was performed with 150 W RF power in an argon pressure of 3.3×10$^{-3}$ Torr for 1.5 hour. During deposition, the quartz substrate was heated to ~300 °C for better adsorption and homogeneity. To improve the crystallinity of the films and achieving the delafossite phase, post-deposition annealing was performed in a high vacuum furnace at 800 °C for 2 hours.

The grazing incidence X-ray diffraction (XRD) analysis was carried to identify the crystalline formation of the sample using a Philips PW1730 instrument. An atomic force microscope (AFM) was used to investigate the Nano hardness of the layer by a Nano Scope III from Digital Instruments AFM. Field emission scanning electron microscopy (FESEM, TESCAN mira 3 xmu) was utilized to realize the morphology of the prepared thin film. Furthermore, the UV-vis transmittance spectrum of the prepared thin film was recorded using an Avantes UV-vis spectrometer (AvaSpec-3648).

3. Results and discussion

In order to investigate the crystal structure of the thin film, we employed XRD analysis in grazing configuration which is shown in Fig 1. As we can see from the figure, the amorphous nature of quartz substrate is clear in the background noise but a distinct peak is
observed at 2θ=36° which corresponds to the formation of CuCrO$_2$ rhombohedral phase alongside the (012) plane. The crystallite size of thin films, calculated by Debye-Scherrer formula was about 25 nm.

In order to investigate the electrical properties of the material, we employed Hall measurement at room temperature in Van-der Pauw configuration. The result of this measurement which is shown in Table 1, indicated the p-type conductivity of the material with the amount of 10.41 S cm$^{-1}$ that is considered a high conductance among p-type TCOs [7].

<table>
<thead>
<tr>
<th>Material</th>
<th>Mobility (cm$^2$ V$^{-1}$ s$^{-1}$)</th>
<th>Hole concentration (cm$^{-3}$)</th>
<th>Conductivity (S cm$^{-1}$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CuCr$<em>{0.975}$Mg$</em>{0.025}$O$_2$</td>
<td>0.018</td>
<td>3.06x10$^{20}$</td>
<td>10.41</td>
</tr>
</tbody>
</table>

The hardness of the deposited CuCrO$_2$ thin film was investigated by nano indent AFM hardness measurement (Fig. 4). The gained hardness was 9.5 GPa which is approximately 950 Hv. This hard layer is in great comparison with previous attempts made for the formation of hard CuCrO$_2$ which had hardness up to 750 Hv [8]. Amorphous Al$_2$O$_3$ coatings have had hardness up to 900 Hv but have only been able to be applied as a hard protective layer solely [9] while the CuCrO$_2$ layers prepared in this research show potential electronic, optical and mechanical properties for the use in touchscreen displays.
4. Conclusion

In this paper, we prepared a p-type TCO with high figure of merit properties. This material showed a high transparency of ~70%, electrical conductivity of ~10.5 S cm⁻¹ and a hardness of 950 Hv. These properties together make this TCO very suitable for application in flat and touch displays that can protect the inside circuitry from damages, and conduct electricity while being invisible to the eye.

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References